

FEATURES

- ▶ Industrial Standard DIP-24 Package
- ▶ Ultra-wide 4:1 Input Voltage Range
- ▶ Fully Regulated Output Voltage
- ▶ Ultra-high Isolation 8000VDC with Reinforced Insulation, rate for 1000Vrms Working Voltage
- ▶ Common Mode Transient Immunity: 15KV/μs
- ▶ Qualified for IGBT and Hi Isolation Applications
- ▶ Operating Ambient Temp. Range -40°C to +85°C
- ▶ Overload and Short Circuit Protection
- ▶ Designed-in Conducted EMI meets EN 55032 Class A & FCC Level A
- ▶ UL/cUL/IEC/EN 60950-1 Safety Approval & CE Marking



PRODUCT OVERVIEW

The MINMAX MIEI03-HI series is a new range of isolated 3W DC-DC converter modules in DIP-24 package which feature a ultra-wide input range, fully regulated output and Ultra-high Isolation voltage rated for 8000VDC with reinforced insulation. A very high common mode transient immunity with 15KV/μs qualifies these product for IGBT driver applications. Further features include overload protection, short circuit protection and EN 55032 class A compliant as well. There are 8 Models available for 24 and 48VDC input. These converters offer a cost-effective solution for wind turbine, solar panel, transporation systems, industrial control equipments and some IGBT driver applications where a very high I/O-isolation is required.

Model Selection Guide

Model Number	Input Voltage (Range)	Output Voltage	Output Current		Input Current		Reflected Ripple Current	Max. capacitive Load	Efficiency (typ.)
			Max.	Min.	@Max. Load	@No Load			
			VDC	VDC	mA	mA			mA(typ.)
MIEI03-24S05HI	24 (9 ~ 40)	5	600	90	162	20	15	1000	77
MIEI03-24S12HI		12	250	37.5	152			470	82
MIEI03-24D12HI		±12	±125	±18.8	151			220#	83
MIEI03-24D15HI		±15	±100	±15	151			220#	83
MIEI03-48S05HI	48 (18 ~ 80)	5	600	90	81	10	8	1000	77
MIEI03-48S12HI		12	250	37.5	76			470	82
MIEI03-48D12HI		±12	±125	±18.8	75			220#	83
MIEI03-48D15HI		±15	±100	±15	75			220#	83

For each output

Input Specifications

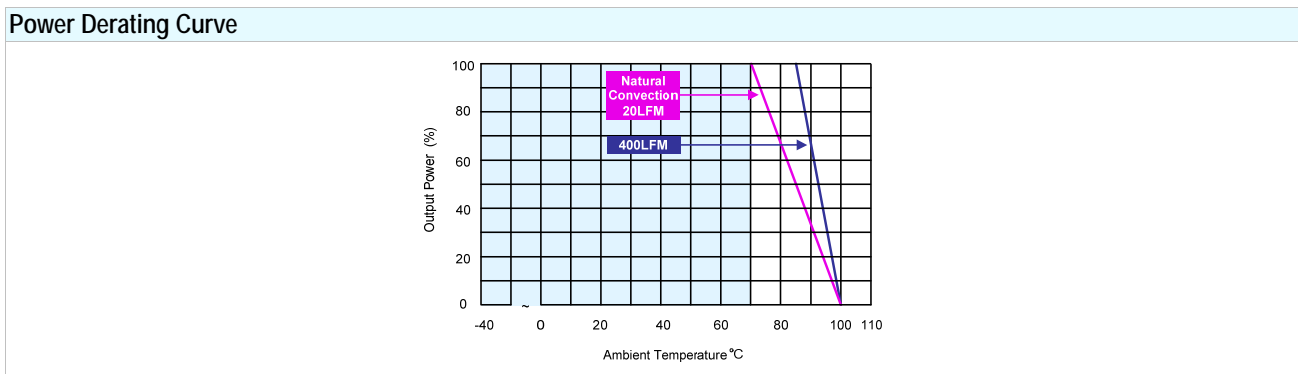
Parameter	Model	Min.	Typ.	Max.	Unit
Input Surge Voltage (1 sec. max.)	24V Input Models	-0.7	---	50	VDC
	48V Input Models	-0.7	---	100	
Start-Up Threshold Voltage	24V Input Models	8	8.5	9	
	48V Input Models	13	15	17	
Under Voltage Shutdown	24V Input Models	---	---	8.5	
	48V Input Models	---	---	16	
Short Circuit Input Power	All Models	---	---	2000	mW
Input Filter		Internal Pi Type			
Conducted EMI		Compliance to EN 55032,class A and FCC part 15,class A			

Output Specifications							
Parameter	Conditions	Min.	Typ.	Max.	Unit		
Output Voltage Setting Accuracy		---	---	±1.0	%Vnom.		
Output Voltage Balance	Dual Output, Balanced Loads	---	±0.5	±2.0	%		
Line Regulation	Vin=Min. to Max. @Full Load	---	±0.3	±0.5	%		
Load Regulation	Io=25% to 100%	---	±0.5	±1.0	%		
Ripple & Noise	0-20 MHz Bandwidth	5V Output Models		---	75	100	mV _{P-P}
		Other Output Models		---	100	150	mV _{P-P}
Transient Recovery Time	25% Load Step Change	---	150	500	μsec		
Transient Response Deviation		---	±3	±6	%		
Temperature Coefficient		---	±0.02	±0.05	%/°C		
Over Load Protection	Foldback	120	150	---	%		
Short Circuit Protection	Continuous						

Isolation, Safety Standards						
Parameter	Conditions	Min.	Typ.	Max.	Unit	
I/O Isolation Voltage	60 Seconds Reinforced insulation, rated for 1000Vrms working voltage	4000	---	---	VACrms	
	Tested for 1 second	8000	---	---	VDC	
I/O Isolation Resistance	500 VDC	10	---	---	GΩ	
I/O Isolation Capacitance	100KHz, 1V	---	7	13	pF	
Common Mode Transient Immunity		15	---	---	KV/μs	
Safety Approvals	UL/cUL 60950-1 recognition(UL certificate), IEC/EN 60950-1(CB-report)					

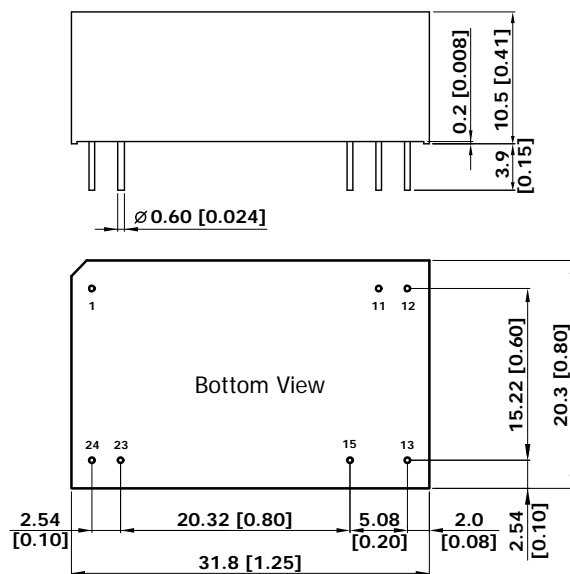
General Specifications						
Parameter	Conditions	Min.	Typ.	Max.	Unit	
Switching Frequency		---	150	---	KHz	
MTBF(calculated)	MIL-HDBK-217F@25°C, Ground Benign	1,000,000			Hours	

Environmental Specifications						
Parameter	Conditions	Min.	Max.	Unit		
Operating Ambient Temperature Range (See Power Derating Curve)	Natural Convection	-40	+85	°C		
Case Temperature		---	+100	°C		
Storage Temperature Range		-50	+125	°C		
Humidity (non condensing)		---	95	% rel. H		
Cooling	Natural Convection					
Lead Temperature (1.5mm from case for 10Sec.)		---	260	°C		



Notes

- 1 Specifications typical at Ta=+25°C, resistive load, nominal input voltage and rated output current unless otherwise noted.
- 2 Transient recovery time is measured to within 1% error band for a step change in output load of 75% to 100%.
- 3 These power converters require a minimum output loading to maintain specified regulation, operation under no-load conditions will not damage these modules; however, they may not meet all specifications listed.
- 4 We recommend to protect the converter by a slow blow fuse in the input supply line.
- 5 Other input and output voltage may be available, please contact factory.
- 6 That "natural convection" is about 20LFM but is not equal to still air (0 LFM).
- 7 Specifications are subject to change without notice.

Package Specifications
Mechanical Dimensions

Pin Connections

Pin	Single Output	Dual Output
1	+Vin	+Vin
11	No Pin	Common
12	-Vout	No Pin
13	+Vout	-Vout
15	No Pin	+Vout
23	-Vin	-Vin
24	-Vin	-Vin

- ▶ All dimensions in mm (inches)
- ▶ Tolerance: X.X±0.5 (X.XX±0.02)
X.XX±0.25 (X.XXX±0.01)
- ▶ Pins ±0.05(±0.002)

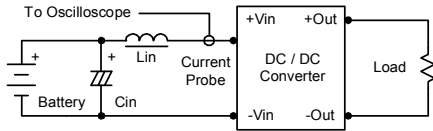
Physical Characteristics

Case Size	: 31.8x20.3x10.5mm (1.25x0.8x0.41 inches)
Case Material	: Non-Conductive Black Plastic (flammability to UL 94V-0 rated)
Pin Material	: Copper Alloy with Gold Plate Over Nickel Subplate
Weight	: 16.2g

Test Setup

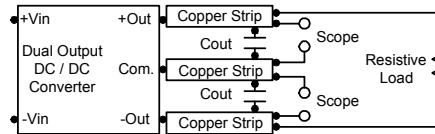
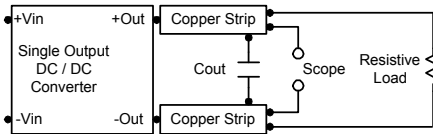
Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor L_{in} ($4.7\mu\text{H}$) and C_{in} ($220\mu\text{F}$, $\text{ESR} < 1.0\Omega$ at 100KHz) to simulate source impedance. Capacitor C_{in} offsets possible battery impedance. Current ripple is measured at the input terminals of the module, measurement bandwidth is $0\text{-}500\text{KHz}$.



Peak-to-Peak Output Noise Measurement Test

Use a C_{out} $0.47\mu\text{F}$ ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is $0\text{-}20\text{MHz}$. Position the load between 50mm and 75mm from the DC-DC Converter.



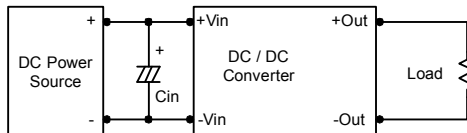
Technical Notes

Overload Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. The unit operates normally once the output current is brought back into its specified range.

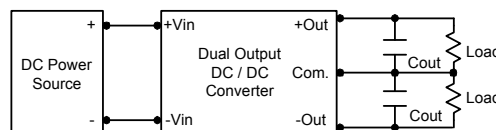
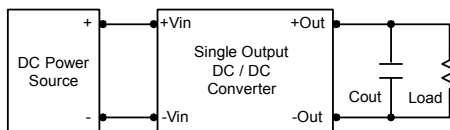
Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor on the input to insure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance ($\text{ESR} < 1.0\Omega$ at 100kHz) capacitor of a $4.7\mu\text{F}$ for the 24V input devices and $2.2\mu\text{F}$ for the 48V devices.



Output Ripple Reduction

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use $3.3\mu\text{F}$ capacitors at the output.



Maximum Capacitive Load

The MIEI03-HI series has limitation of maximum connected capacitance on the output. The power module may operate in current limiting mode during start-up, affecting the ramp-up and the startup time. Connect capacitors at the point of load for best performance. The maximum capacitance can be found in the data sheet.

Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 100°C . The derating curves are determined from measurements obtained in a test setup.